

10/551391

JC12 Rec'd PCT/PTO 29 SEP 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: SASAO, Tsutomu, et al.

Serial Number: Not Yet Assigned
(§371 of International Application PCT/JP2004/004752)

Filed: September 29, 2005

For. PROGRAMMABLE LOGIC DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 29, 2005

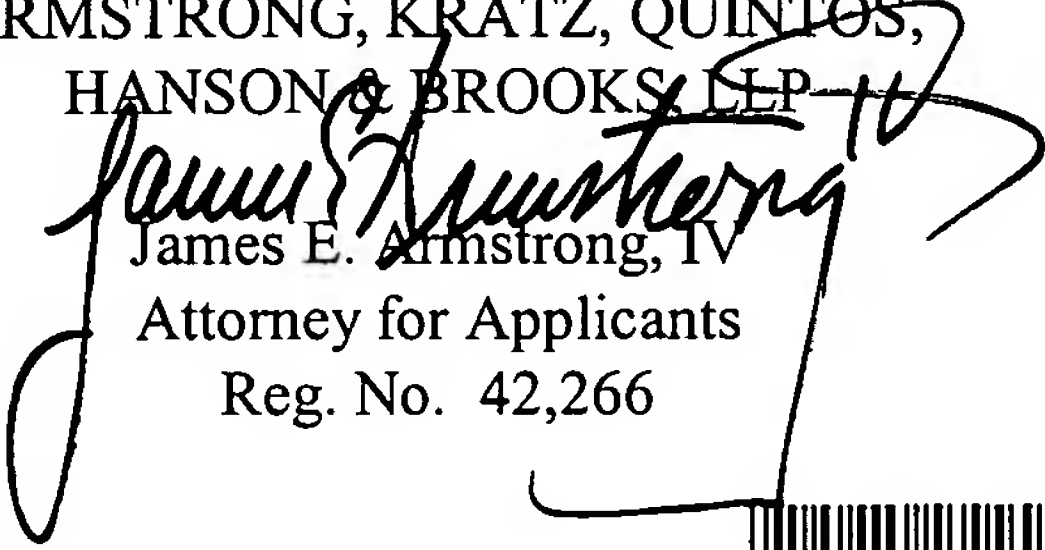
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449 and cited in the enclosed international search report.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; International Search Report

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INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 050637	Serial No. New Application.
	Applicant(s): SASAO, Tsutomu, et al.	
	Filing Date: September 29, 2005	Group Art Unit: Not yet assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____	AE		
_____	AF		
_____	AG		
_____	AH		
_____	AI		

OTHER DOCUMENTS

_____	AJ	Tsutomu SASAO et al., "Tashutsuryoku Kansu no Cascade Jutsugen to Saikosei Kano Hardware ni yoru Jitsugen", The institute of Electronics, Information and Communication Engineers Gijutsu Kenkyu Hokoku, Vol. 101, No. 3, April 6, 2001, pages 57-64; Cited in the international search report.		
_____	AK	Akihiko TOMITA et al., "LUT Array-gata PLD no Sekkei to Shisaku", The Institute of Electronics, Information and Communication Engineers Gijutsu Kenkyu Hokoku, Vol. 100, No.475, November 23, 2000, Pages 173-178; Cited in the international search report.		
<table border="1"> <tr> <td>Examiner</td> <td>Date Considered</td> </tr> </table>			Examiner	Date Considered
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